



## ● Key Feature

- SiRF Star IV with external Flash solution
- Embedded InstantFix CGEE and Reverse CGEE(3 days)
- Anti-jamming : Removes in-band jammers up to 80 dB-Hz, track up to 8 CW jammers ensure maximum performance even in hostile signal environments – GSM, NB environments.
- MEMS Sensor interface support – Flexible platform for now and future MEMS application.

## ● Electrical Specification

GPS receiver	
Chipset	SiRF Star IV, GSD4e – 9411
Frequency	L1,1,575.42 MHz
Channels	48 track verification channels
C/A Code	1,023 MHz
Chipset Sensitivity	-Autonomous Acquisition -148dBm -Navigation Sensitivity -160dBm -Tracking Sensitivity -163dBm
Chipset Fix time (Open SKY,typical)	- Reacquisition: less than 1s - Hot start: less than 1s - Warm start: less than 35s - Cold start: less than 35s
Accuracy	Position: 2.5meters (50% 24hr static, -130dBm)
Interface Protocol	NMEA0183 standard
LNA	SiRF chip Embedded 1 stage LNA
Flash	16 Mb
Dynamics -Altitude	18288 meter maximum
Dynamics -Velocity	514 meter/second maximum
Module I/O Pin	SMT type with stamp holes, 22pins
Power	
Voltages	1.8v (Max. 1.89v)
Current	Typ.60mA@1.8V(Average Acquisition w/o ext. Antenna) Typ.42mA@1.8V(Average Tracking w/o ext. Antenna)
Hibernate Mode	Typ.20uA@1.8V(Average w/o ext. Antenna)
Size/Environment	
Dimension (mm)	13*15*2.2
Weight	≤3g
Operating/storage temperature	-30 ~ 85°C / -40 ~ 85°C
Humidity	≤95%